

FIG. 1

10

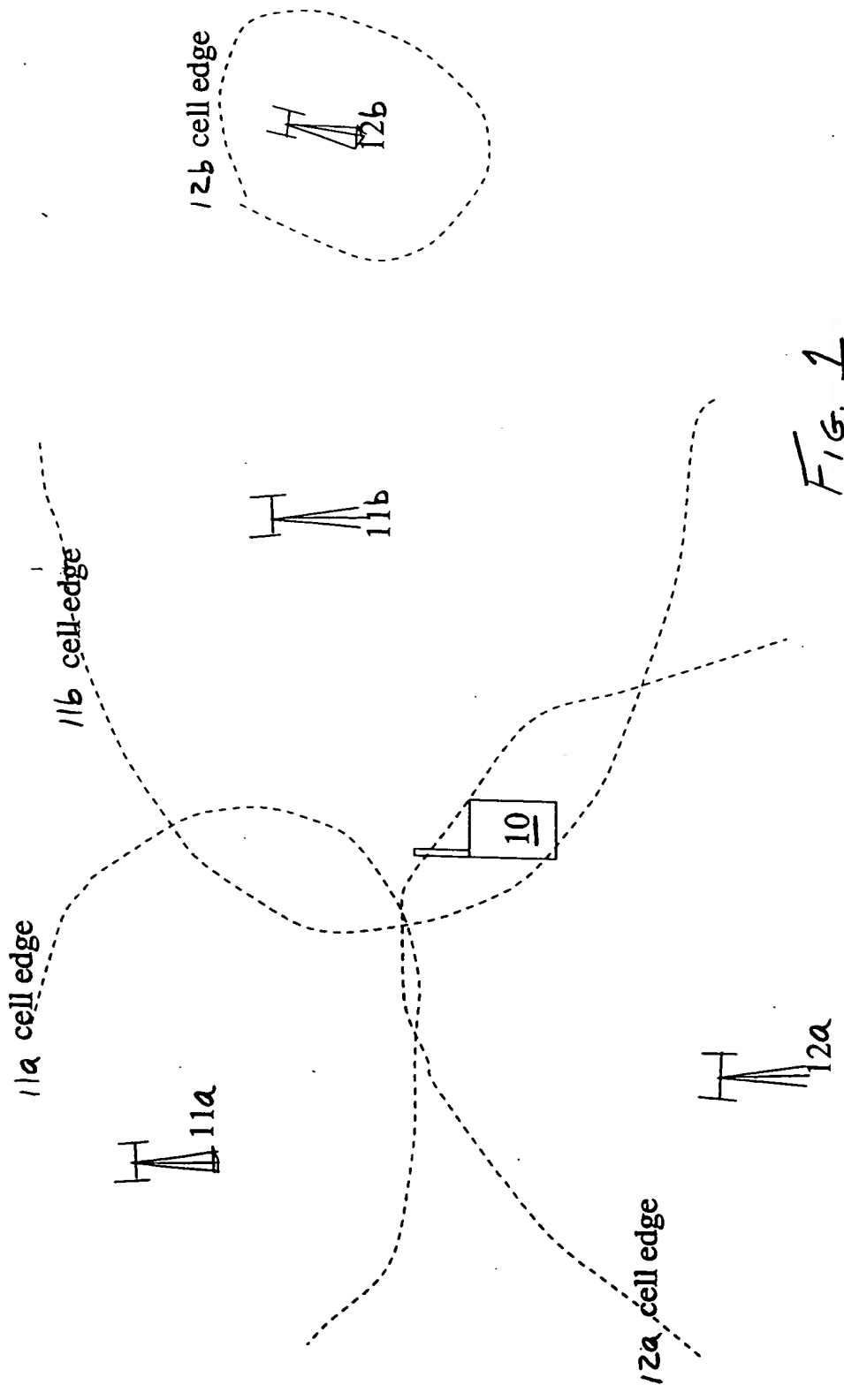


Fig. 1

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14

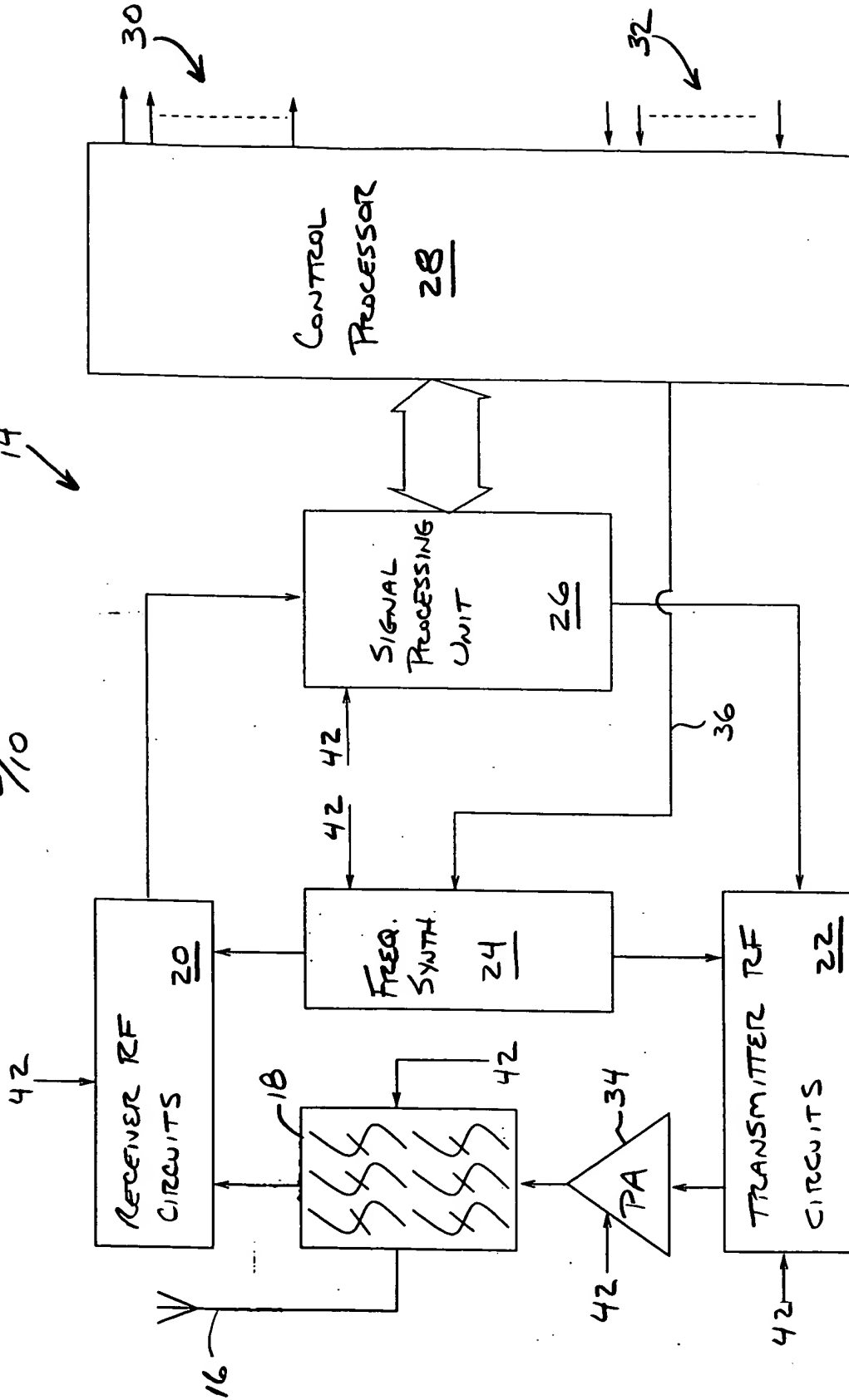
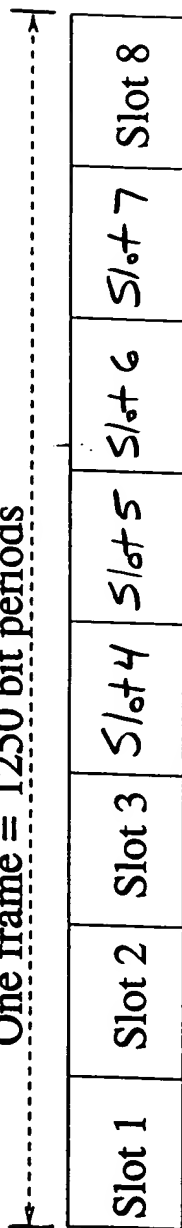


FIG. 2 (Prior Art)

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FIG. 3

One frame = 1250 bit periods



43

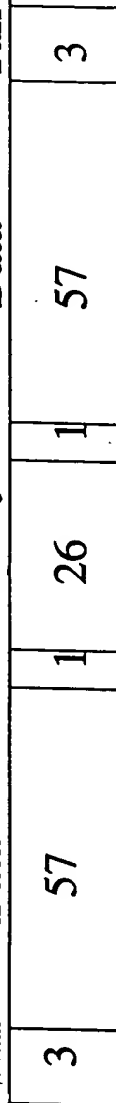
Traffic Data

Sync. Data

Traffic Data

Tail

Tail



4

4.25

Flag

Flag

57

57

1

1

3

3

DYNAMIC  
TIME  
ALIGNMENT

UP/DOWN  
RAMPING

One slot = 156.25 bit periods

44

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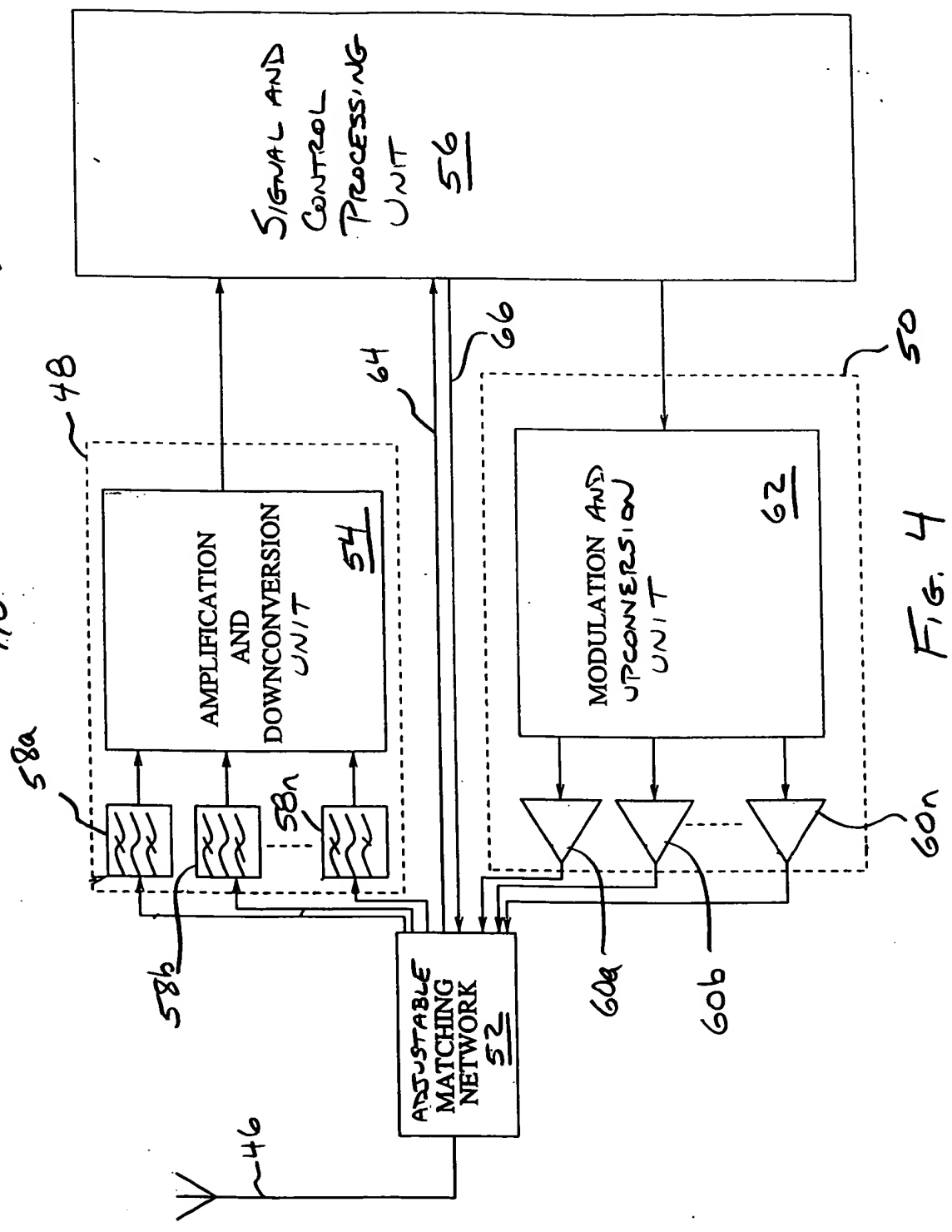


Fig. 4

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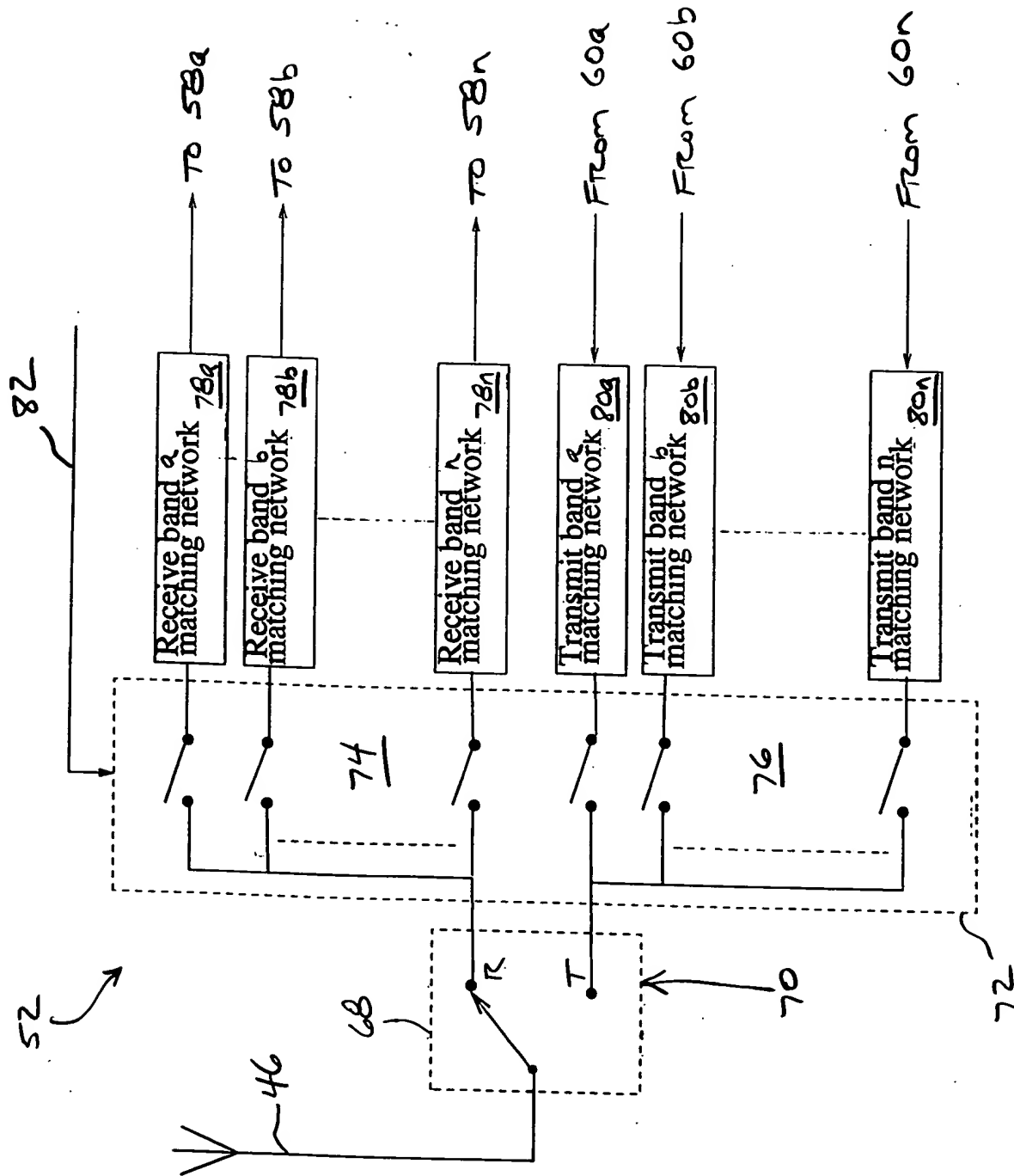


Fig. 5

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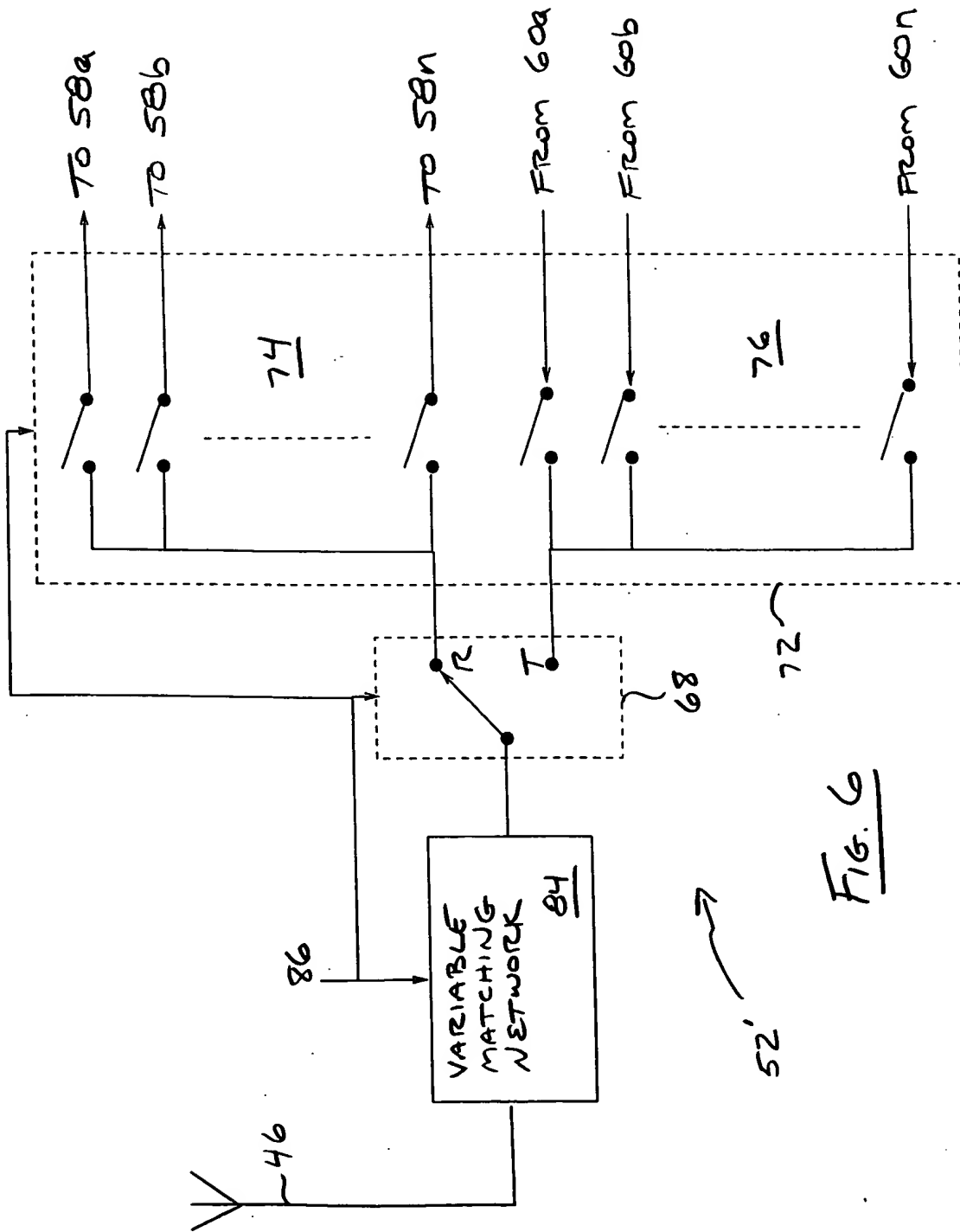


FIG. 6

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52" →

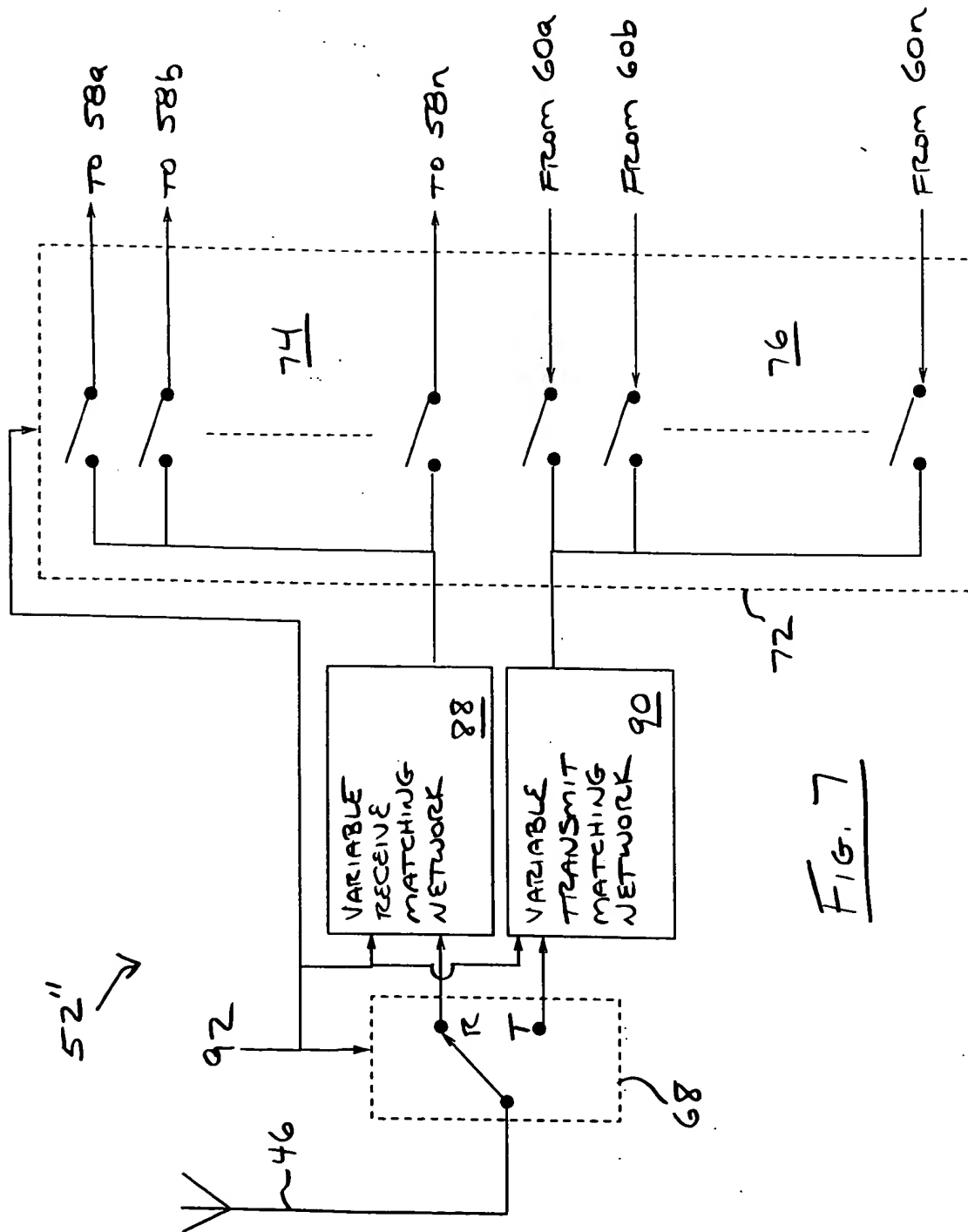


FIG. 7

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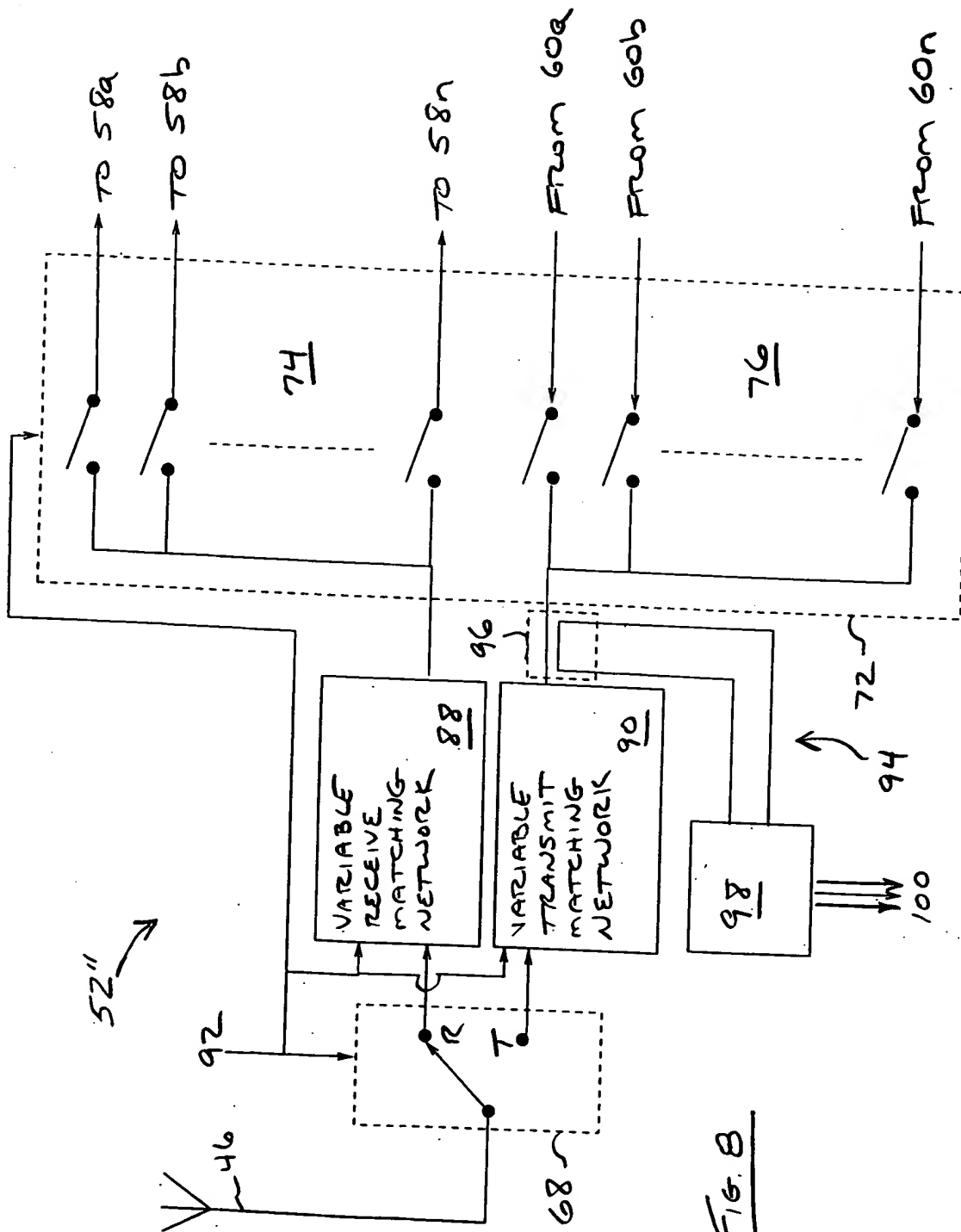


Fig. 8



TC 9210-1080/250

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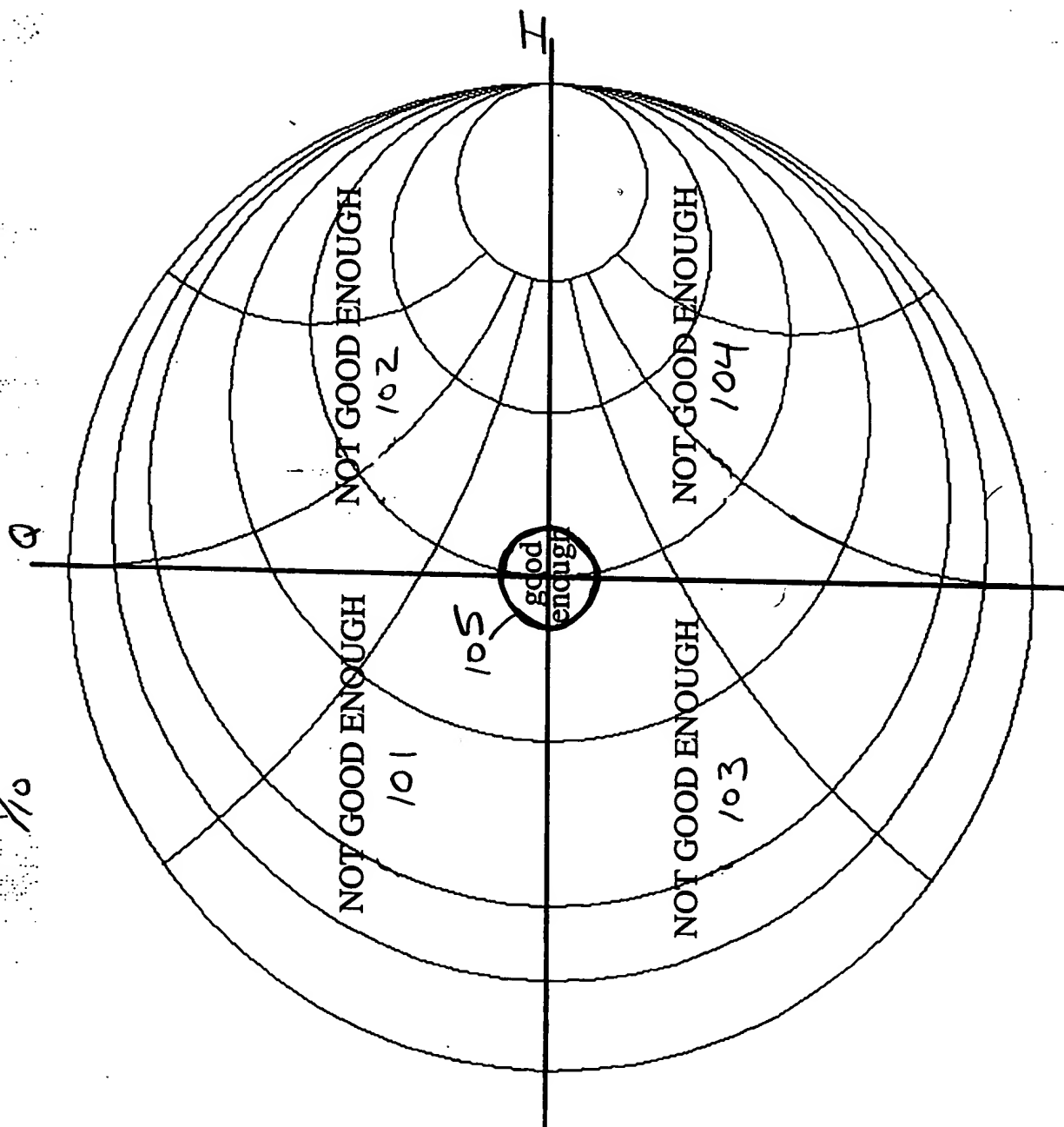


Fig. 9

TO  
T/R SWITCH  
68

